

MODÈLES HIÉRARCHIQUES POUR LA VÉRIFICATION EFFICACE DE SYSTÈMES TEMPS-RÉEL

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RÉSEAUX DE CAPTEURS...

F. Kordon — LIP6/MoVe — UPMC

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Modèles Hiérarchiques pour la vérification efficace de systèmes temps-réel — Rabat, Septembre 2013

- **Avant tout un système réparti**
 - Communications filaires ou non
- **Des contraintes embarquées**
 - Empreinte mémoire, consommation
- **Autres contraintes**
 - Mobilité, Temps-réel, fiabilité, variabilité du milieu...
- **Dimensions variables**
 - Quelques cm... quelques mm... bientôt moins?

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- Besoins en certification
 - Analyse des capacités du système à effectuer ses missions
 - Systèmes complexes = éviter les comportement inattendus

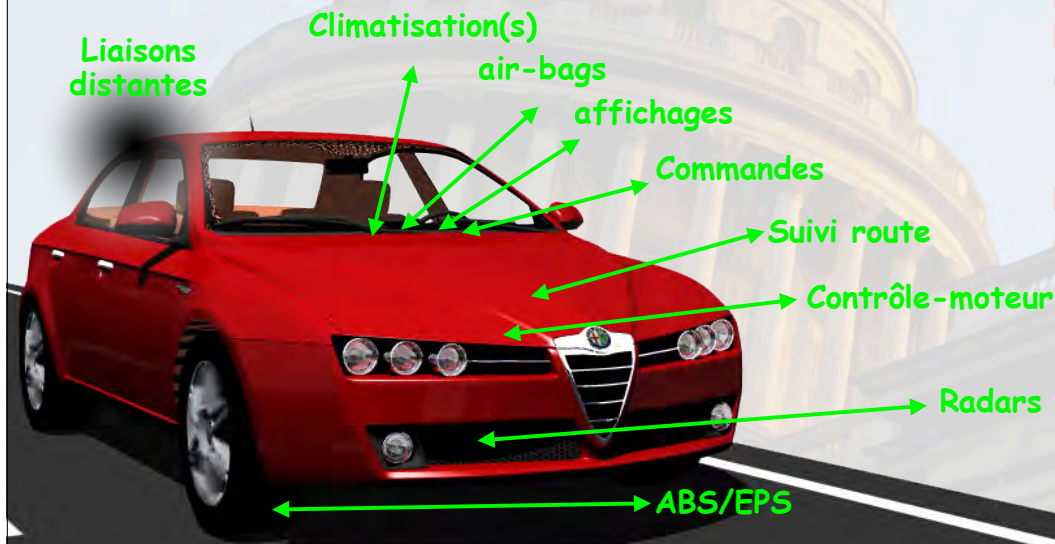
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*Mais avant tout...
C'est un TR²E...*

- 🔦 Un véhicule = un système réparti embarqué
- 🔦 Fonctions locales nombreuses : AUTOSAR = middleware
- 🔦 Système critique => Fiabilité
- 🔦 Liaisons distantes (IVI = In-Vehicle Infotainment)

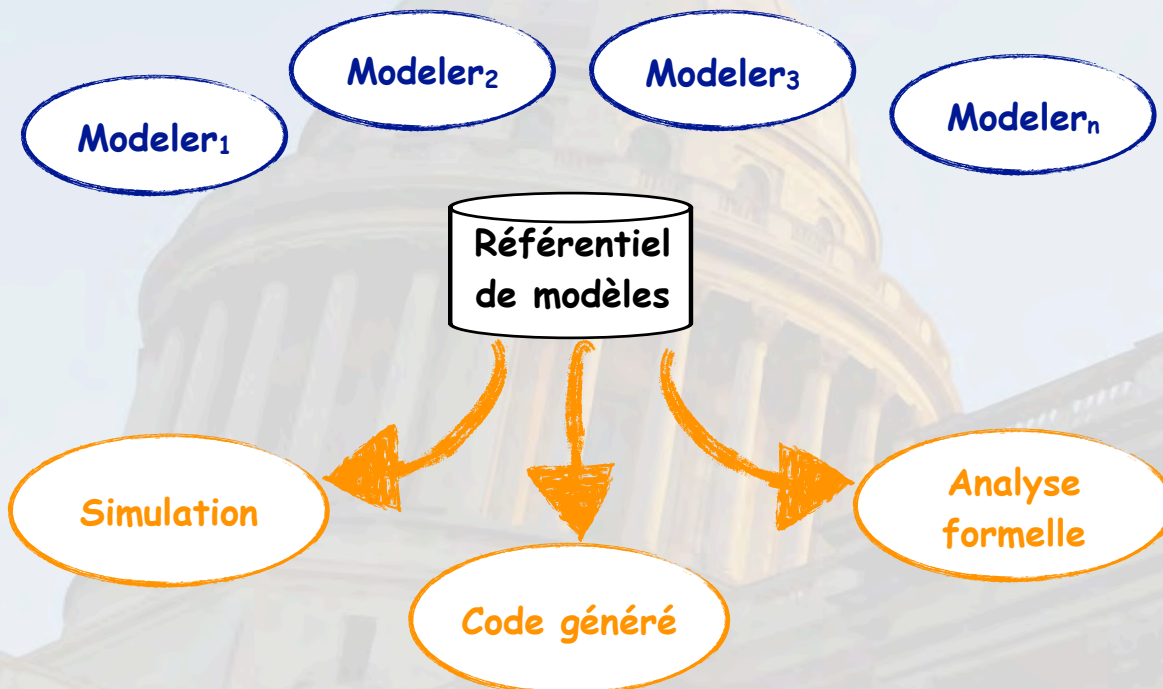
Temps
Réel
Réparti
Embarqué

Distributed
Réal-time
Embodied

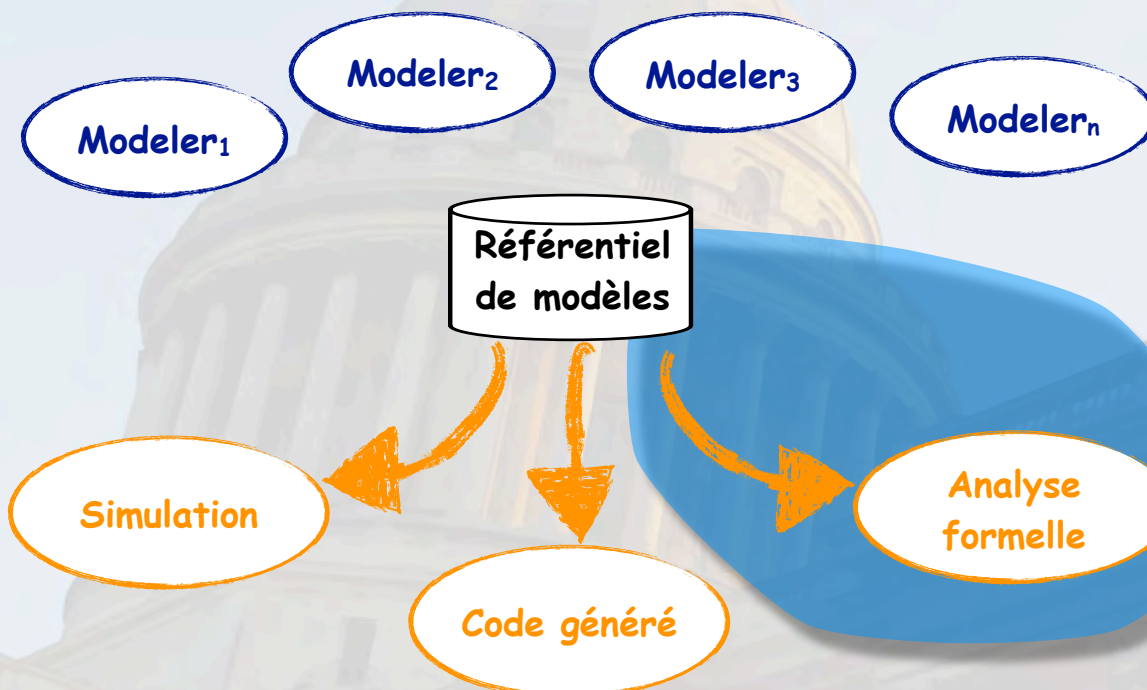


- 🔦 Automatiser = prévenir les incertitudes du développement

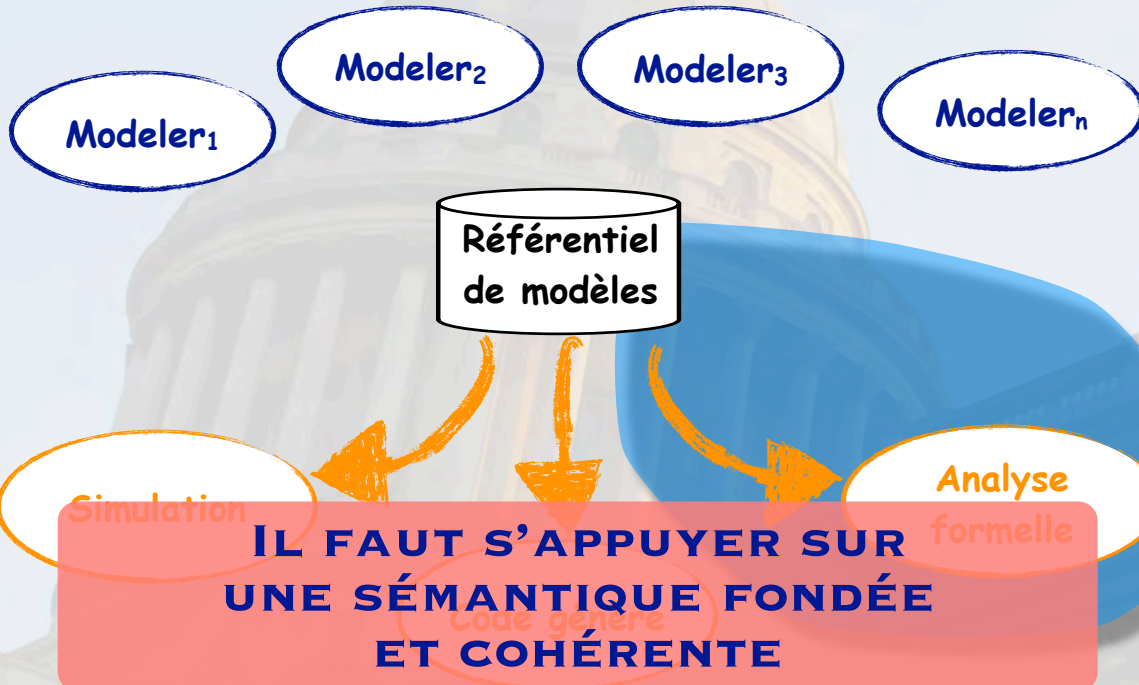
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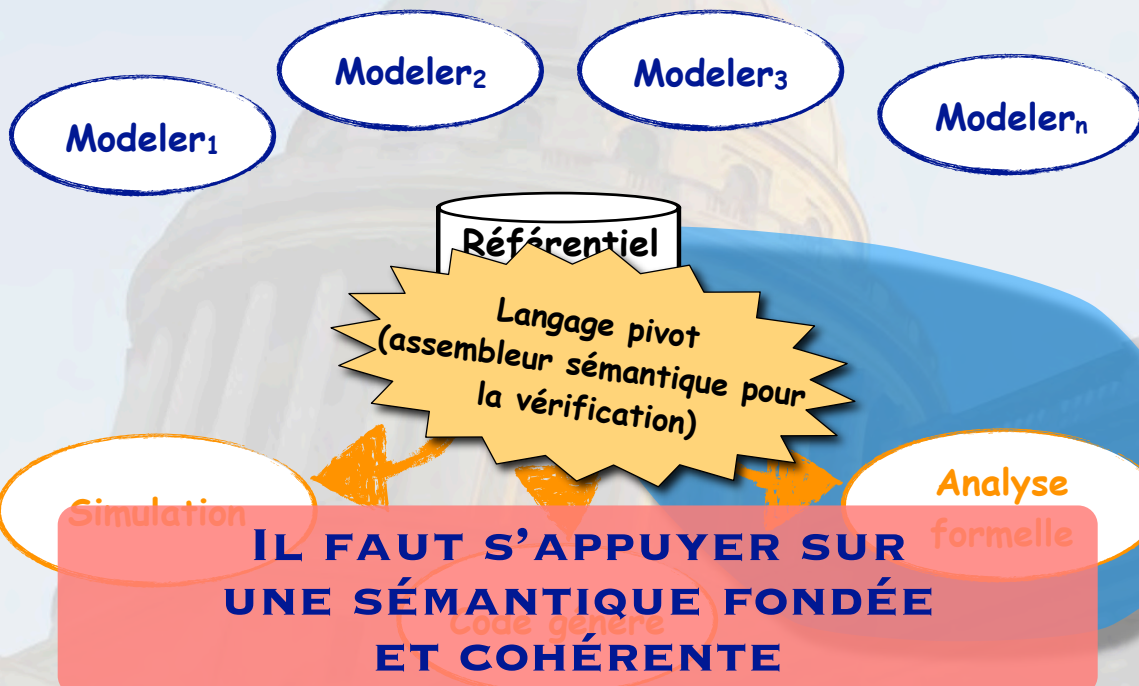
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Preuve formelle (approche «algébrique»)

-  Paramétré
-  Peu automatisé
-  Diagnostic difficile

Analyse Statique

-  Approche statique (structure plus simple)
-  Capture du comportement délicate (vision structurelle)

Model Checking

-  Automatisable
-  Diagnostic aisé
-  Explosion combinatoire

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Approches complémentaires

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Sujet de
cet
exposé

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Sujet de
cet
exposé

Ennemis du model checking:
1) Consommation mémoire
2) consommation CPU

TR²E = systèmes complexes

● Interoperabilité

- Communications asynchrones => Complexité



● Dynamicité

- Pas forcément de borne => encore plus de complexité



● Symétries

- Schéma de répétition dans le système
- Permutabilité de certains éléments



● Structuration

- Traiter la localité dans certains systèmes
- Partager des portions communes des configurations



● Hiérarchie

- Meilleure organisation et gestion des «répétitions»



● Quelques observations sur ces systèmes complexes

- Exploiter les «bonnes» caractéristiques

● Une proposition: «Instantiable Transition Systems»

- Sans temps
- Encodage du temps

● Liaison à une approche «MDE»

● Éléments de conclusion...

... avant exposé suivant

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Une proposition: «Instantiable Transition Systems»

- Sans temps

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Liaison à une approche «MDE»

Éléments de conclusion

Transparents
suivants en
anglais



et exposé suivant

OBSERVATIONS

HIERARCHY IS GOOD FOR DISTRIBUTED SYSTEMS

[HONG 2012]

Based on the notion of locality [Bryant 1986] + [Clarke 1992]

- State = vector of integers
- Share of common parts in a set of states

Sequence of variable affectations

- Accepting sequence: $x \leftarrow a$, $y \leftarrow 1$ and $x \leftarrow a$, $y \leftarrow 2$

Exponential gain in favorable cases

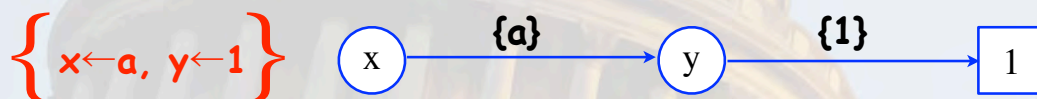
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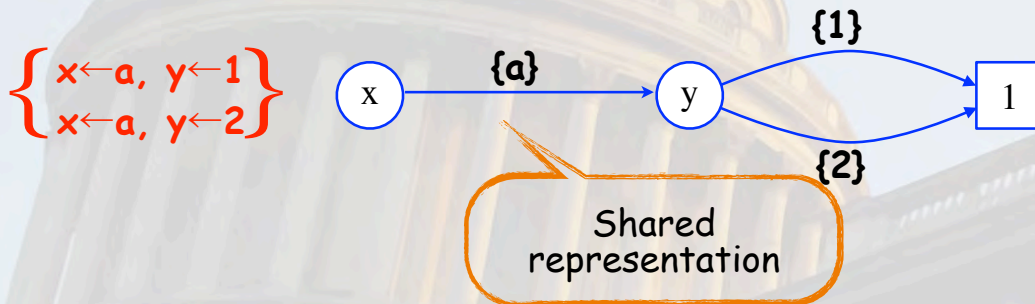
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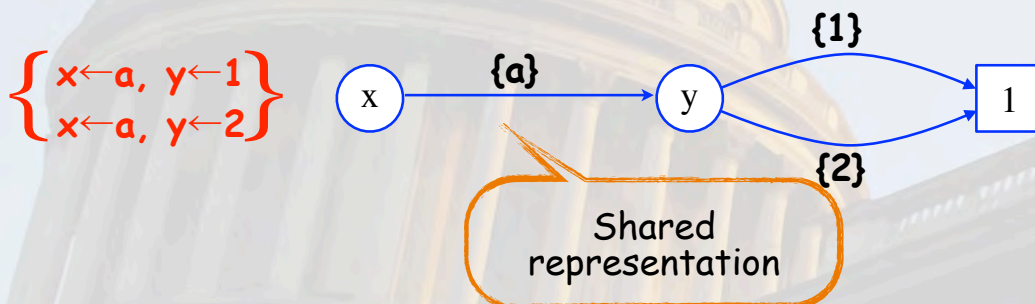
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- Order of the variable encoding is crucial

Hierarchical Decision Diagrams [Couvreur 2005]

- Arcs labeled by sets
- A decision diagram represents a set
- Recursivity

Example

- Structured data: $\langle p1, \{1, 3\} \rangle + \langle p2, \{1, 3\} \rangle$

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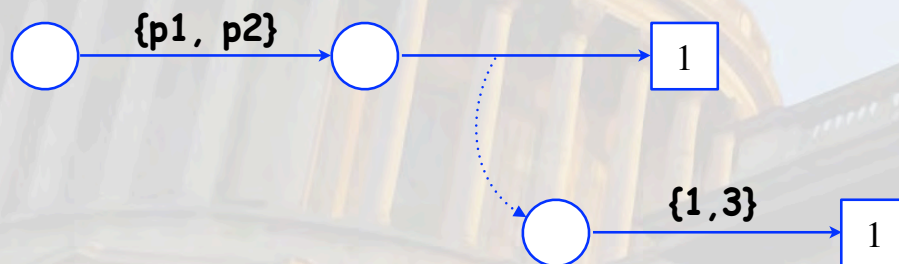


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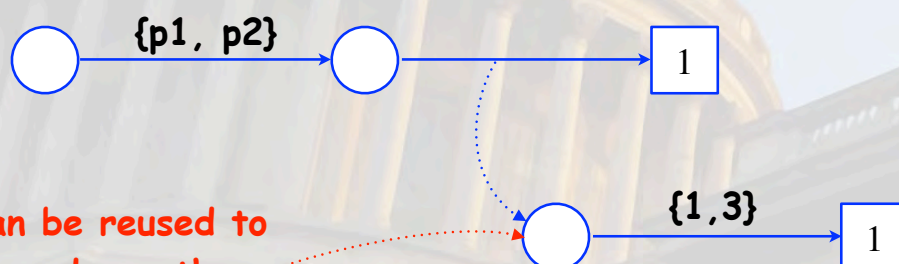


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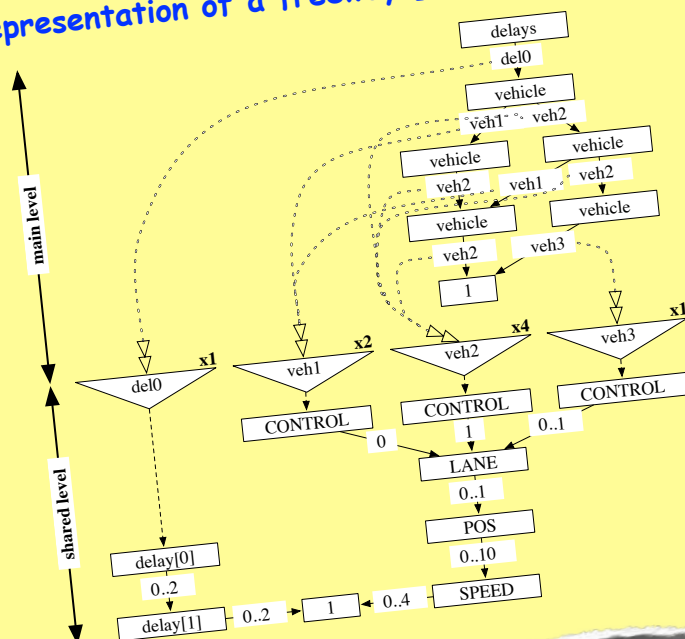
- Structured data: $\langle p1, \{1, 3\} \rangle + \langle p2, \{1, 3\} \rangle$



Can be reused to
encode another
part of the system

Hierarchical
Representation of a freeway [Bérard 2008]

vreur 2005]



, 3}

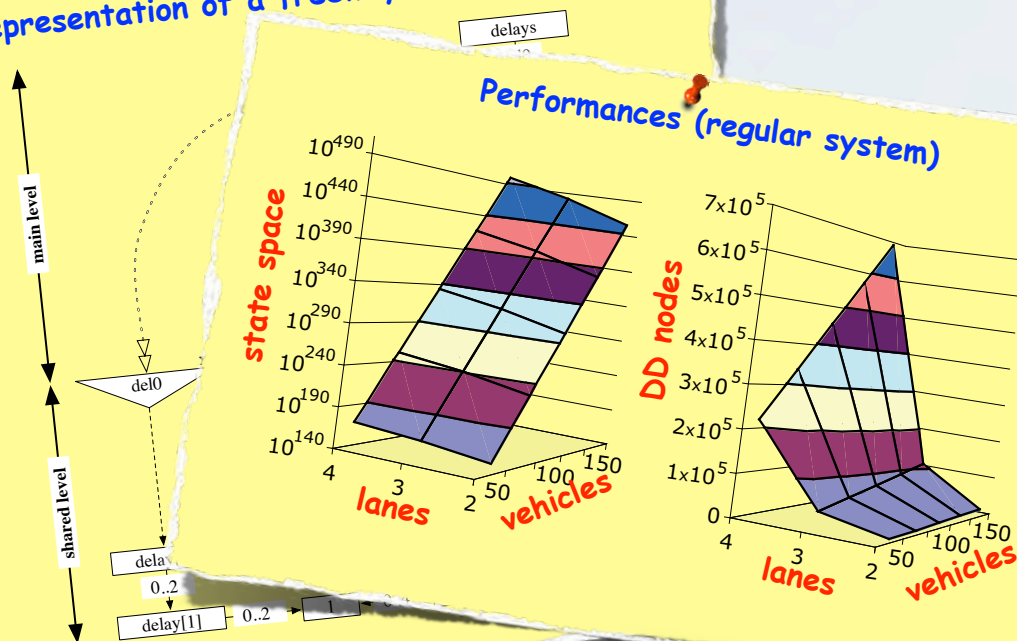
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Hierarchical Representation of a freeway [Bérard 2008]

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vreur 2005]

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Objective: exploit the structure of large Petri Nets

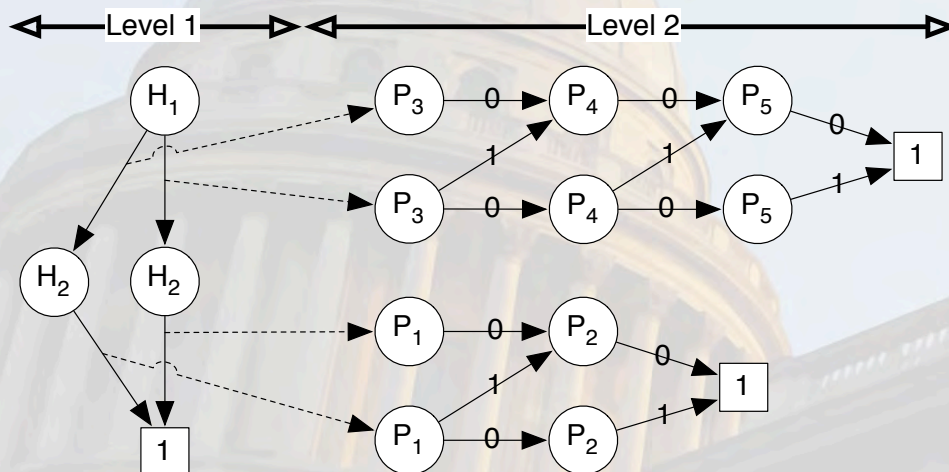
- Obtained from «unfolding of Colored Nets»
- Characteristics: large models with repeated patterns

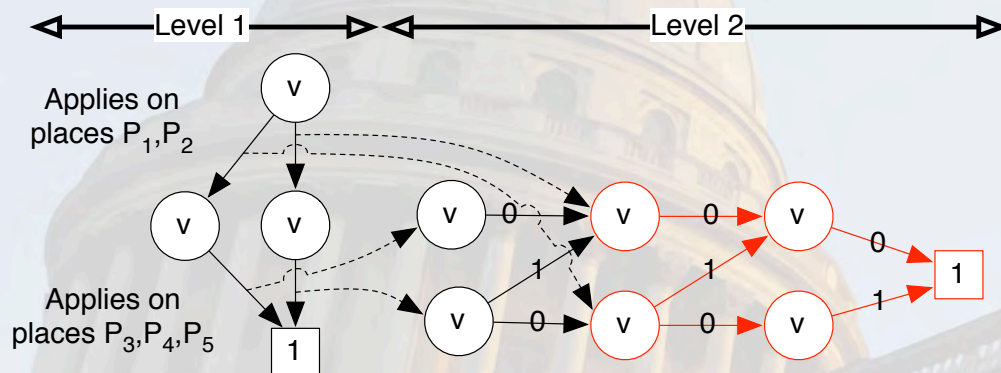
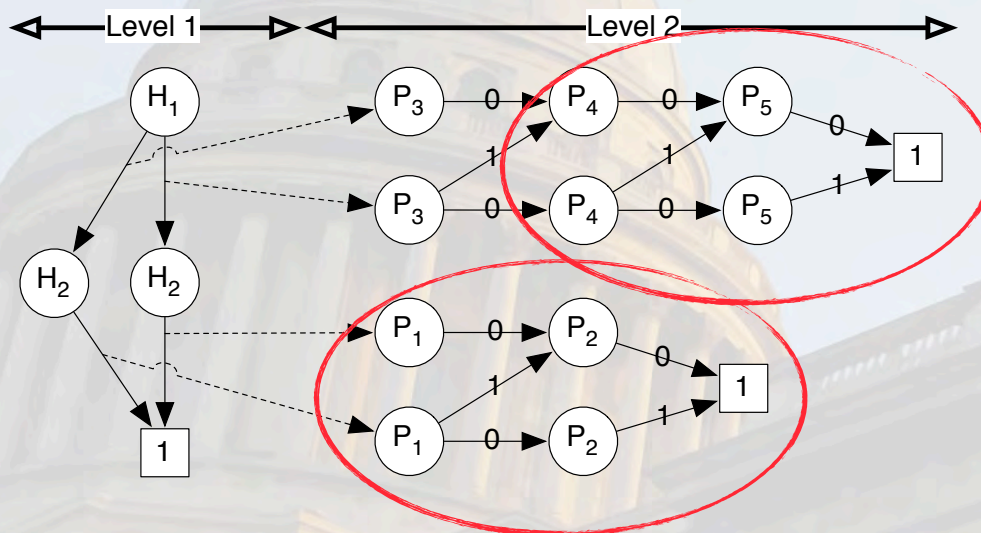
Involved techniques

- Compute a variable order
 - FORCE [Aloul 2003] or NOA99 [Heiner 2009]**
- Hierarchically cluster this order

Anonymize the clusters as much as possible

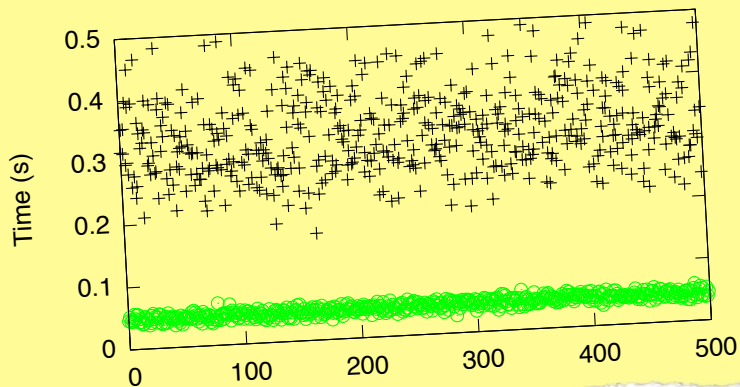
- Anonymization = contextual interpretation
- Reused patterns contextually





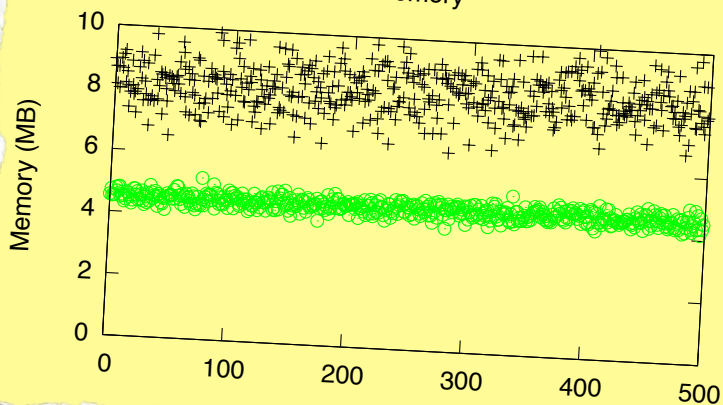
Comparing to random order

CPU Time



Comparing to random order

Memory



PolyORB case Study (1 state = up to 2KB)

number of Instances	State Space Size	Flat	Flat order performance				Hierarchical order performance				Gain (in %)			
			Final	Peak	Time	MB	Final	Peak	Time	MB	Final	Peak	Time	Mem
2	1.6×10^6	F	223,243	3.1×10^6	580.8	1,316	27,548	491,803	29.3	352	88	84	95	73
		N	78,785	451,494	98.7	273	10,050	127,791	6.2	81	87	72	94	70
3	2.8×10^7	F	593,363	1.2×10^7	4,708	2,851	78,067	2.1×10^6	188.7	692	87	83	96	76
		N	280,068	2.5×10^6	948.8	1,513	33,526	524,288	64.3	358	88	79	93	76
4	2.1×10^8	F	1.2×10^6	3.1×10^7	8,310	5,765	146,589	4.2×10^6	528.7	1,780	87	86	94	69
		N	666,886	8.4×10^6	217,173	2,457	84,126	2.1×10^6	326.0	1,451	87	75	99	41
5	1.4×10^9	F	TOF	TOF	TOF	TOF	143,903	1.1×10^7	2,361.6	3,045	∞	∞	∞	∞
		N	TOF	TOF	TOF	TOF	87,875	4.2×10^6	1,045.1	2,216	∞	∞	∞	∞
6	9.2×10^9	F	TOF	TOF	TOF	TOF	288,649	2.2×10^7	15,757	6,144	∞	∞	∞	∞
		N	TOF	TOF	TOF	TOF	140,565	1.5×10^7	19,474	4,865	∞	∞	∞	∞
7	-	F	TOF	TOF	TOF	TOF	MOF	MOF	MOF	MOF	-	-	-	-
		N	TOF	TOF	TOF	TOF	TOF	TOF	TOF	TOF	-	-	-	-

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		N	TOF	TOF	TOF	TOF	TOF	TOF	TOF	TOF	-	-	-	-

NEOPPOD case Study (1 state = up to 2.4KB)

number of Instances	State Space Size	Flat	Flat order performance				Hierarchical order performance				Gain (in %)			
			Final	Peak	Time	MB	Final	Peak	Time	MB	Final	Peak	Time	Mem
2	194	F	202	679	0.024	4.1	80	217	0.007	3.42	68	68	71	17
		N	463	1,688	0.024	4.5	154	558	0.011	3.57	67	67	55	21
3	90,861	F	5,956	48,269	0.86	19.0	1,126	12,491	0.15	8.2	81	74	82	57
		N	3,820	23,974	0.76	12.2	709	4,838	0.10	6.7	81	80	87	45
4	9.7×10^8	F	84,398	1.0×10^6	62.6	338.8	11,728	178,921	5.0	70.83	86	82	92	79
		N	155,759	1.3×10^6	186.1	490.3	19,875	217,536	0.007	114.2	87	83	99	77

- Combination of techniques can help tackling complexity
 - MCC@Petri Net 2013 (as well as in 2011 and 2012) showed this
 - Complex distributed systems enable «good techniques»
 - Suitable for Systems of Systems
 - Complexity: «additive logic» instead of «product logic»
- But Distributed systems are also Embedded
 - OK for the main characteristics...
 - ... but we need time!
- Need for a formal assembly language to manage time
 - ITS (Instantiable Transition Systems) [Thierry-Mieg 2009]
 - Combine: symmetries + decision diagrams + saturation
 - How to extend for time!

INSTANTIABLE TRANSITION SYSTEMS AND TIME

[THIERRY-MIEG 2011]



ITS IN A NUTSHELL..

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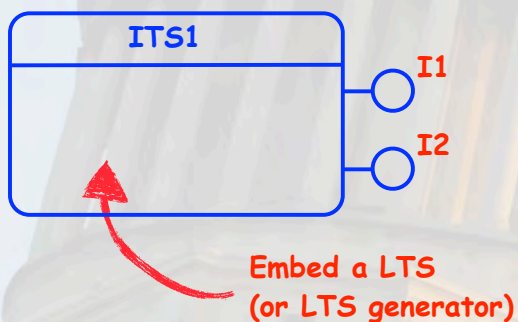
Modèles Hiérarchiques pour la vérification efficace de systèmes temps-réel — Rabat, Septembre 2013



A formal framework for verification



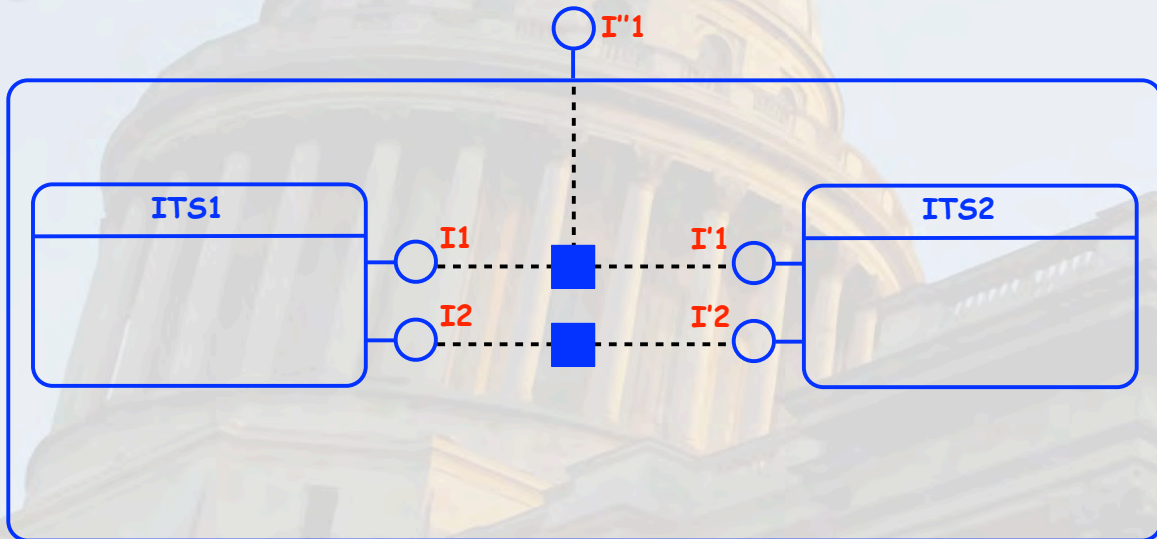
Elementary ITS





A formal framework for verification

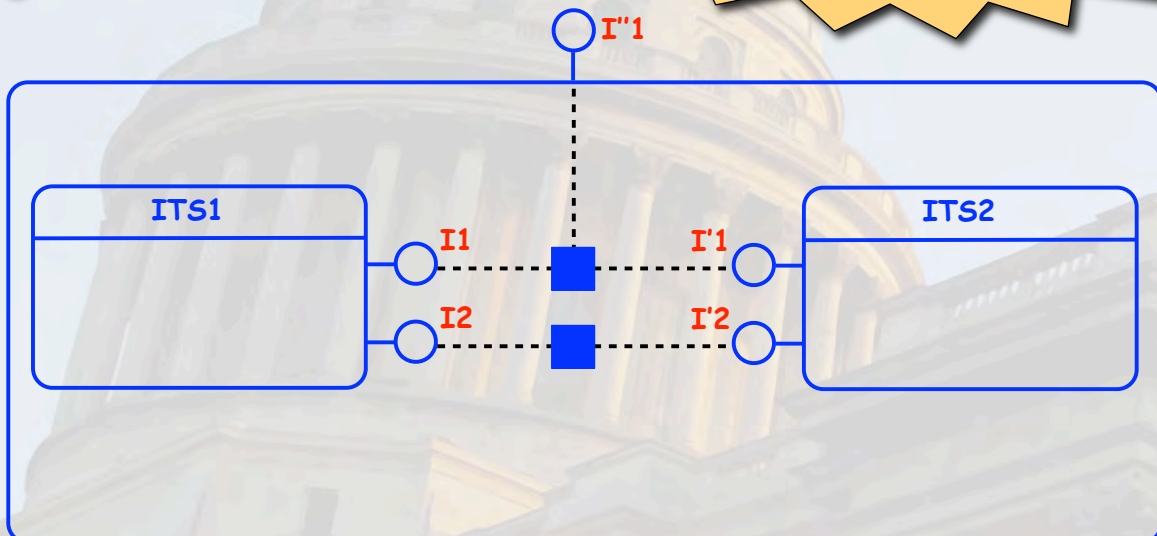
- Elementary ITS
- Composite ITS
- Recursion...



A formal framework for

- Elementary ITS
- Composite ITS
- Recursion...

Advantages:
Express locality
Define instances
Hierarchy



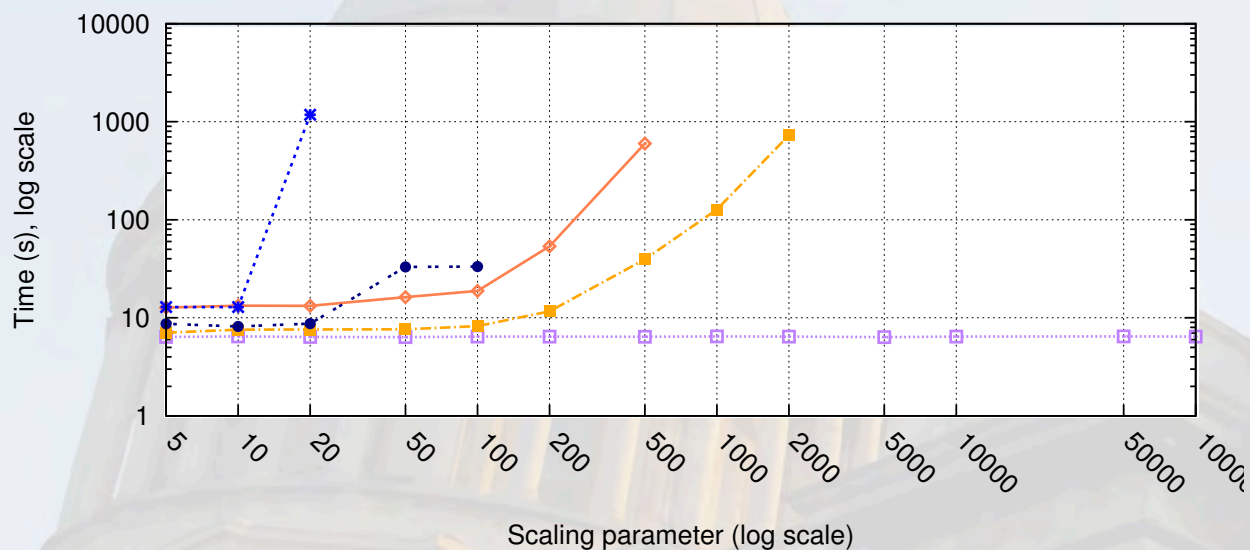


The «recursive unfolding» technique



The «recursive unfolding» technique

CPU for state space generation (Philosophers)

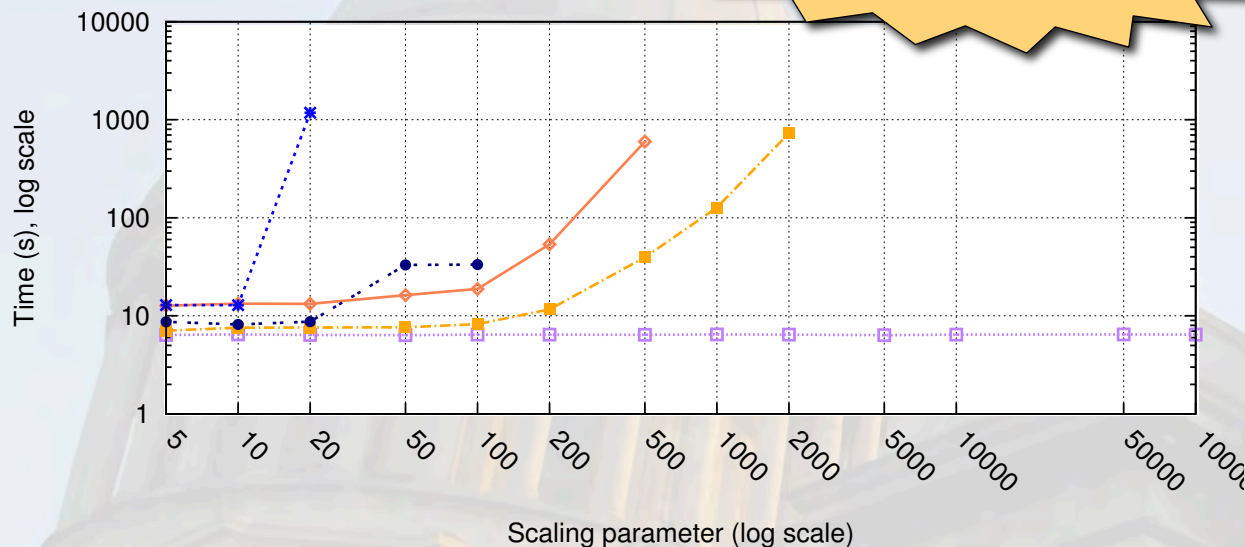




The «recursive unfolding» technique

CPU for state space generation

Memory limit : 4GB
CPU limit: 1h
None reached with ITS



24/06/2012, 11:26

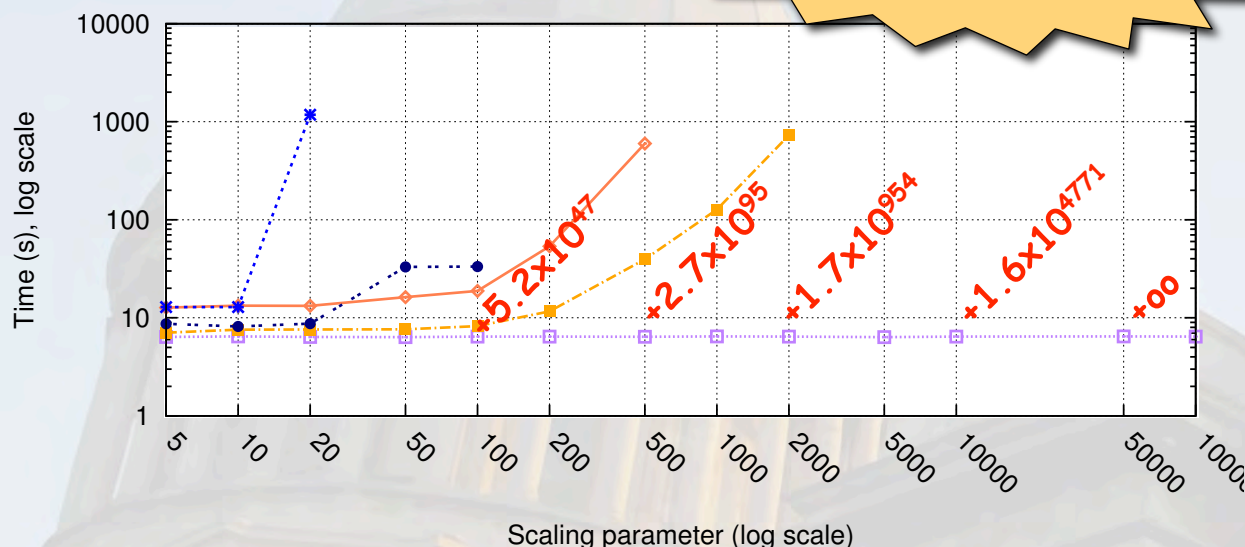
AIPiNa — Helena — ITS-Tools — Marcie — PNXDD



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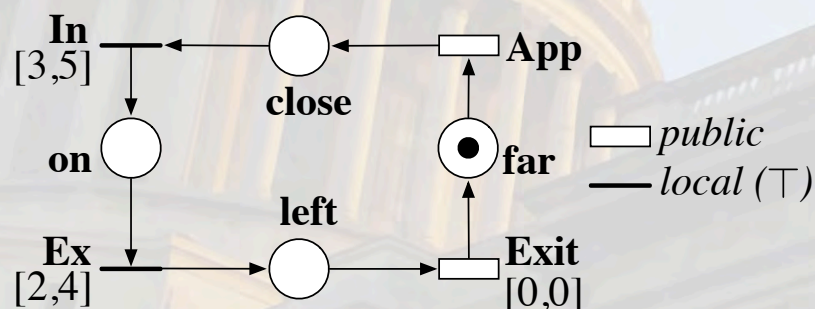
Discrete time can be encoded in a similar way

- An extra interface : $\mathbb{1}$
- All $\mathbb{1}$ are synchronized
- Caution: consistency of synchronized time constraints
 - Only synchronize untimed transitions?
 - Only synchronize transitions with the same time constraints?

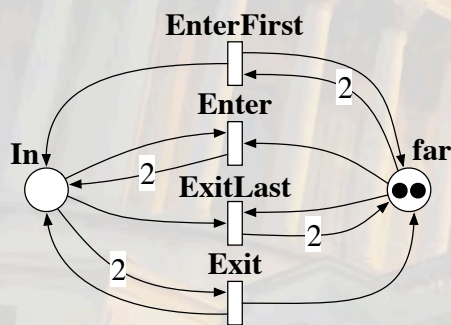
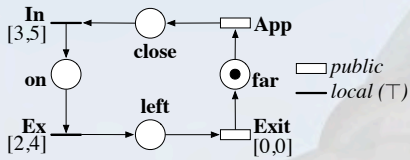
Then, all the underlying tricks can be activated

- Deduction of locality
- Various encoding patterns
- Rewriting mechanisms to enable «saturation»
- etc...

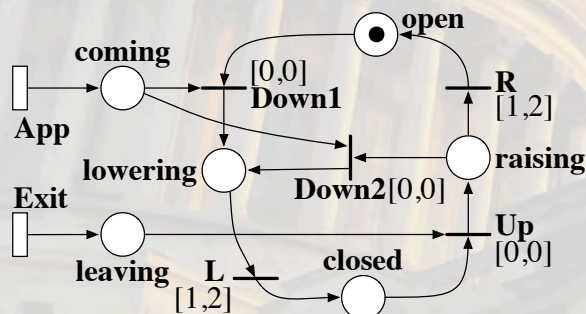
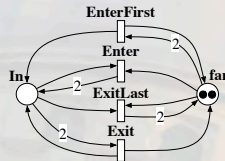
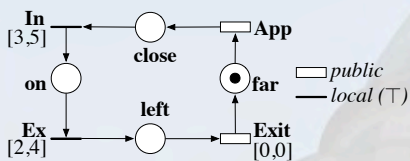
Here, ITS embed Time Petri Nets



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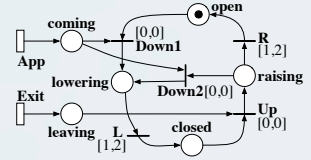
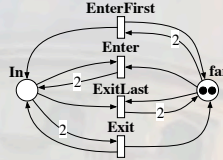
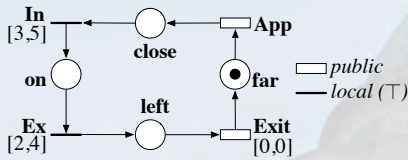


Here, ITS embed Time Petri Nets





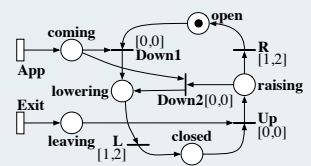
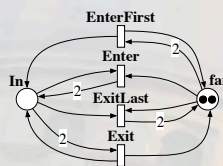
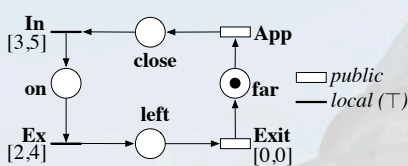
Here, ITS embed Time Petri Nets



t_0 : train	t_1 : train	λ
App	ε	App
ε	App	App
Exit	ε	Exit
ε	Exit	Exit
\perp	\perp	\perp



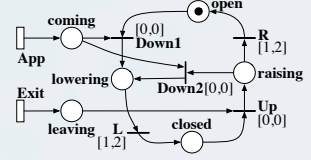
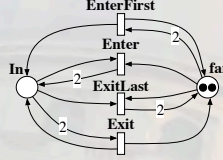
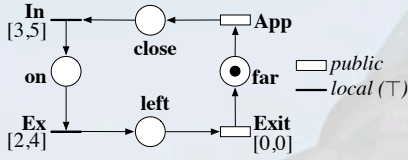
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t_0 : train	t_1 : train	λ	tg : TrainGroup	cc : ContrGate	λ
App	ε	App	Exit	Exit	\top
ε	App	App	App	App	\top
Exit	ε	Exit	\perp	\perp	\perp
ε	Exit	Exit			
\perp	\perp	\perp			



Here, ITS embed Time Petri Nets



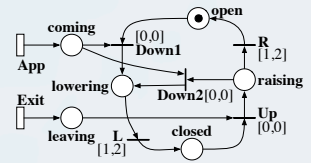
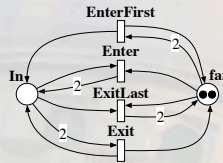
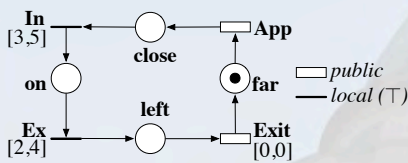
t_0 : train	t_1 : train	λ
App	ε	App
ε	App	App
Exit	ε	Exit
ε	Exit	Exit
$\mathbb{1}$	$\mathbb{1}$	$\mathbb{1}$

tg : TrainGroup	cc : ContrGate	λ
Exit	Exit	\top
App	App	\top
$\mathbb{1}$	$\mathbb{1}$	$\mathbb{1}$

g : gate	c : controller	λ
App	EnterFirst	App
ε	Enter	App
Exit	ExitLast	Exit
ε	Exit	Exit
$\mathbb{1}$	$\mathbb{1}$	$\mathbb{1}$



Here, ITS embed Time Petri Nets



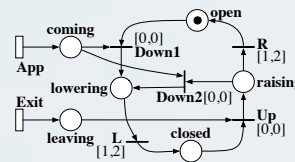
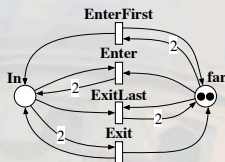
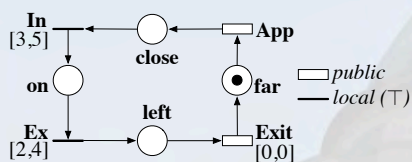
t_0 : train	t_1 : train	λ
App	ε	App
ε	App	App
Exit	ε	Exit
ε	Exit	Exit
$\mathbb{1}$	$\mathbb{1}$	$\mathbb{1}$

tg : TrainGroup	cc : ContrGate	λ
Exit	Exit	\top
App	App	\top
$\mathbb{1}$	$\mathbb{1}$	$\mathbb{1}$

g : gate	c : controller	λ
App	EnterFirst	App
ε	Enter	App
Exit	ExitLast	Exit
ε	Exit	Exit
$\mathbb{1}$	$\mathbb{1}$	$\mathbb{1}$



Here, ITS embed Time Petri Nets



Global synchronization for time elapse

(may reduce some symmetries)

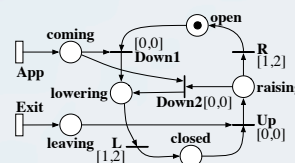
t_0 : train	t_1 : train	λ
App	ε	App
ε	App	App
Exit	ε	Exit
ε	Exit	Exit
1	1	1

tg : TrainGroup	cc : ContrGate	λ
Exit	Exit	1
App	App	1
1	1	1

g : gate	c : controller	λ
App	EnterFirst	App
ε	Enter	App
Exit	ExitLast	Exit
ε	Exit	Exit
1	1	1



Here, ITS embed Time Petri Nets



Groups of 4 trains?

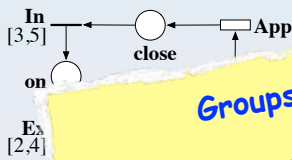
t_0 : train	t_1 : train	t_2 : train	t_3 : train	λ
App	ε	ε	ε	App
ε	App	ε	ε	App
ε	ε	App	ε	App
ε	ε	ε	App	App
Exit	ε	ε	ε	Exit
ε	Exit	ε	ε	Exit
ε	ε	Exit	ε	Exit
1	1	1	1	1

Global synchronization for time elapse

(may reduce some symmetries)

g : gate	c : controller	λ
App	EnterFirst	App
ε	Enter	App
Exit	ExitLast	Exit
ε	Exit	Exit
1	1	1

Here, ITS embed Time Petri Nets



Groups of 4 trains?

Recursive unfolding possible
 $2 \times (2 \times (2 \times (\text{train}))) \dots$

for time elapse

(symmetries)

t_0 : train	t_1 : train	t_2 : train	t_3 : train	λ
App	ϵ	ϵ	ϵ	App
ϵ	App	ϵ	ϵ	App
ϵ	ϵ	App	ϵ	App
ϵ	ϵ	ϵ	App	App
Exit	ϵ	ϵ	ϵ	Exit
ϵ	Exit	ϵ	ϵ	Exit
ϵ	ϵ	Exit	ϵ	Exit
ϵ	Exit	ϵ	Exit	Exit
ϵ	ϵ	ϵ	Exit	Exit
1	ϵ	ϵ	1	1
1	1	1	1	1

g: gate	c: controller	λ
App	EnterFirst	App
ϵ	Enter	App
Exit	ExitLast	Exit
ϵ	Exit	Exit
1	1	1

Here, ITS embed Time Petri Nets



Integration in Romeo, good performances against known tools

Train (N is the number of trains)

N	Roméo			RED		UPPAAL/sym			Roméo/SDD		
	tm	mm	sm	tm	mm	tm	mm	sm	tm	mm	sm
6	43.1	36 948	29 640	7	202 412	0.14	908	432	1.5	7 360	$4.83 \cdot 10^6$
7	6 115	377 452	131 517	66	723 428	0.23	3 200	957	2.5	10 304	$6.28 \cdot 10^7$
8	DNF	-	-	-	OOM	1	3 336	2 078	4	14 188	$8.16 \cdot 10^8$
13	-	-	-	-	-	2 634	13 188	79 598	26	56 660	$3.02 \cdot 10^{14}$
15	-	-	-	-	-	60 860	61 256	-	42	86 360	$5.11 \cdot 10^{16}$
16	-	-	-	-	-	DNF	-	-	52	104 848	$6.65 \cdot 10^{17}$
44	-	-	-	-	-	-	-	-	1143	$2.13 \cdot 10^6$	$1.03 \cdot 10^{49}$

1 1 1

1 1 1



Here, ITS embed Time Petri Nets

EnterFirst

open

Integration in Romeo, good performances against known tools

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N	Roméo			RED		UPPAAL/sym			Roméo/SDD		
	tm	mm	sm	tm	mm	tm	mm	sm	tm	mm	sm
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16	-	-	-	-	-	DNF	-	-	52	104 848	$6.65 \cdot 10^{17}$
44	-	-	-	-	-	-	-	-	1143	$2.13 \cdot 10^6$	$1.03 \cdot 10^{49}$

1

1

1

1

1

1



Here, ITS embed Time Petri Nets

EnterFirst

open

Integration in Romeo, good performances against known tools

Train (N is the number of trains)

N	Roméo			RED		UPPAAL/sym			Roméo/SDD		
	tm	mm	sm	tm	mm	tm	mm	sm	tm	mm	sm
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7	6 115	377 452	131 517	66	723 428	0.23	3 200	957	2.5	10 304	$6.28 \cdot 10^7$
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Here, ITS embed Time Petri Nets

Integration in Romeo, good performances against known tools

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N	Roméo			RED		UPPAAL/sym			Roméo/SDD		
	tm	mm	sm	tm	mm	tm	mm	sm	tm	mm	sm
6	43.1	36 948	29 640	7	202 412	0.14	908	432	1.5	7 360	$4.83 \cdot 10^6$
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8	DNF	-	-	-	OOM	1	3 336	2 078	4	14 188	$8.16 \cdot 10^8$
13	-	-	-	-	-	2 634	13 188	79 598	26	56 660	$3.02 \cdot 10^{14}$
15	-	-	-	-	-	60 860	61 256	-	42	86 360	$5.11 \cdot 10^{16}$
16	-	-	-	-	-	DNF	-	-	52	104 848	$6.65 \cdot 10^{17}$
44	-	-	-	-	-	-	-	-	1143	$2.13 \cdot 10^6$	$1.03 \cdot 10^{49}$

1

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1

1

1

1

TOWARDS GENERATION FROM A HIGH-LEVEL LANGUAGE

VeriSensor (Domain Specific Modeling Language)

- Dedicated to sensor networks

- Proposes relevant concepts

Compilation into ITS

- Seeking for the activation of the presented mechanisms

Good scalability

VeriSensor (Domain Specific Modeling Language)

- Dedicated to sensor networks
- Proposes relevant concepts

Compilation into ITS

- Seeking for the activation of the presented mechanisms

Good scalability

More Tomorrow

- Ph.D. Defense of Yann Ben Maissa
- Tomorrow, 10h00, same place 😊

CONCLUSION...

... AND ADVERTIZING

Combination of techniques to handle discrete time

Hierarchy + Symmetries + Decision Diagrams

Shows good scalability so far!



Relation with the System architecture

Structure can be exploited

Potential «assembly language» for Verification

Distributed systems

Timing constraints

Implementation available

<http://ddd.lip6.fr>

Library & tool (graphic interface with Eclipse)

Reads Romeo & TINA nets...

Some of the presented tools embedded in the CosyVerif project

<http://cosyverif.org>

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